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PROGRAM

Nova 800 Teletype Test

TAPES

Binary 095-000048-01

ABSTRACT

The Nova 800 Teletype Test is a maintenance program designed to detect malfunctions in the teletype logic, interrupt system, and the I/O bus logic. The program may be used to test teletype models ASR33, KSR33, and KSR35. The program contains routines to punch and read random data, echo typed keys, punch from the switch register, etc.

NOVA-800 TELETYPE TEST

11. ABSTRACT  
THIS IS A MAINTENANCE PROGRAM DESIGNED TO DETECT MALFUNCTIONS IN THE TELETYPE LOGIC, INTERRUPT SYSTEM, AND THE I/O BUS LOGIC. THE PROGRAM MAY BE USED TO TEST TELETYPE MODELS ASR33, KSR33, AND KSR35. THE PROGRAM CONTAINS ROUTINES TO PUNCH AND READ RANDOM DATA, ECHO TYPED KEYS, PUNCH FROM THE SWITCH REGISTER, ETC.
12. MACHINE REQUIREMENTS  
12.1 NOVA-800 PROCESSOR  
12.2 2K READ/WRITE MEMORY  
12.3 ASR33, KSR33, OR KSR35 TELETYPE
13. SWITCH SETTINGS  
13.0 000006 =NEW DEVICE CODE STARTING ADDRESS  
13.1 000050 =DIAGNOSTIC STARTING ADDRESS  
13.2 000051 =PRINT C(SWITCHES)R ON TELETYPE  
13.3 000052 =ECHO TYPED INPUT  
13.4 000053 =PRINT CHARACTER SET  
13.5 000054 =PUNCH AND READ RANDOM DATA  
13.6 000055 =PUNCH AND READ COUNTER  
13.7 SWITCH 0(1) =PROCEED FROM A ERROR  
13.8 SWITCH 1(1) =SET FOR KSR TELETYPES  
13.9 SWITCH 2(1) =DON'T CHECK KEYBOARD PARITY
14. OPERATING PROCEDURE  
14.0 LOAD THE PROGRAM VIA THE BINARY LOADER, USE THE HIGH SPEED READER IF AVAILABLE.  
14.1 IF THE DEVICE CODES OF THE UNIT UNDER TEST ARE NOT 10 AND 11, ENTER THE EVEN NUMBER IN REGISTER 000005, AND START AT 000006. THE PROGRAM WILL MODIFY ITSELF AND HALT READY FOR THE NEXT STEP.  
14.2 TESTING A ASR33 TELETYPE  
14.2.1 SET THE TELETYPE TO THE LOCAL POSITION  
14.2.2 PRESS THE TAPE PUNCH ON BUTTON  
14.2.3 PRESS THE "HERE IS" KEY. THE PUNCH SHOULD FEED BLANK TAPE.  
14.2.4 PRESS THE "REPT" KEY, THEN THE "RUB OUT" KEY. EXAMINE THE TAPE FOR ALL HOLES.  
14.2.5 PERFORM THE ABOVE TWO STEPS UNTILL ABOUT 18 INCHES OF TAPE HAVE BEEN PUNCHED.  
14.2.6 SET THE TELETYPE SWITCH TO LINE.  
14.2.7 PLACE THE END OF THE PUNCHED TAPE IN THE READER. A TAPE LOOP ABOUT ONE FOOT LONG IS THUS FORMED BETWEEN THE READER AND THE PUNCH.  
14.2.8 SET THE READER SWITCH TO THE START POSITION.

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14.2.9      SET THE SWITCH REGISTER TO 000050
14.2.10     PRESS START
14.2.11     THE PROGRAM WILL RUN UNTILL MANUALLY
)           STOPPED OR A ERROR IS DETECTED. THE WORD
)           "PASS" WILL BE PRINTED AT THE END OF EACH
)           PASS. OTHER INDETERMINATE CODES WILL ALSO
)           BE PRINTED.
14.2.12     WHEN THE PROGRAM HAS COMPLETED SEVERAL
)           PASSES SET THE DATA SWITCHES TO 000053.
14.2.13     PRESS RESET
14.2.14     SET THE READER SWITCH TO FREE.
14.2.15     PRESS THE PUNCH OFF BUTTON
14.2.16     PRESS START
14.2.17     A SUBSET OF THE ASCII CHARACTOR SET WILL
)           BE PRINTED. THE SET BEGINS WITH THE SEQ-
)           UENCE "MMM" TO PERMIT CARRIAGE RETURN
)           ADJUSTMENTS ON A WIDE LETTER.
14.2.18     AFTER SEVERAL LINES HAVE BEEN PRINTED
)           PRESS RESET AND EXAMINE THE PRINTED DATA.
14.2.19     SET THE DATA SWITCHES TO 000052
14.2.20     PRESS START
14.2.21     STRIKE EACH KEY ON THE KEYBOARD AND CHECK
)           TO INSURE THAT IT IS ECHOED PROPERLY.
14.2.22     SET THE DATA SWITCHES TO 000054
14.2.23     PRESS RESET.
14.2.24     REMOVE THE PUNCHED TAPE FROM THE
)           READER AND PUNCH.
14.2.25     PRESS THE PUNCH ON BUTTON
14.2.26     PRESS START
14.2.27     INSERT THE LEADER OF ALL ZEROS INTO
)           THE READER.
14.2.28     SET THE READER SWITCH TO START.
14.2.29     THE PROGRAM SHOULD PUNCH AND READ RANDOM
)           DATA UNTILL MANUALLY STOPPED OR A ERROR
)           IS DETECTED.
14.3        TELETYPES WHICH DO NOT CONTAIN THE READER AND
)           PUNCH OPTIONS MAY BE TESTED BY SETTING DATA
)           SWITCH 1(1) AND OMITTING THOSE STEPS PERTAIN-
)           ING TO THE READING AND PUNCHING OF TAPE.

15.        PROGRAM OUTPUT/ERROR DISCRIPTION
15.1       WHEN A ERROR IS DETECTED BY THE DIAGNOSTIC
)           THE PROGRAM WILL HALT AT LOCATION ERR+6.
)           EXAMINE AC3 TO OBTAIN THE ADDRESS OF THE
)           FAILING ROUTINE.
)           CONSULT THE LISTING TO DETERMINE THE CAUSE
)           OF THE ERROR.
)           PRESSING CONTINUE WILL CAUSE THE PROGRAM
)           TO ENTER A FAILING LOOP SUITABLE FOR SCOPING.
)           SETTING SWITCH 2(1) WILL CAUSE THE PROGRAM
)           TO PROCEED FROM THE ERROR.
15.2       WHEN A HALT IS EXECUTED IN ROUTINES OTHER
)           THAN THE DIAGNOSTIC CONSULT THE LISTING FOR
)           THE CAUSE OF ERROR.

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)5.3      PUNCH SWITCHES ROUTINE (START ADDRESS 51)
)          THIS ROUTINE MAY BE USED FOR SCOPING THE
)          TELETYPE OUTPUT LOGIC. THE ROUTINE TRANS-
)          MITTS A CHARACTER EVERY 102MS INDEPENDENT
)          OF THE TELETYPE BUSY OR DONE FLAG. THE
)          CHARACTER TRANSMITTED IS A FUNCTION OF THE
)          RIGHT HALF DATA SWITCHES.
)5.4      TERMINATING A LINE INPUTTED IN THE ECHO
)          TEST WITH "CTRL Z" WILL CAUSE THE LINE TO
)          BE REPEATED UNTILL ANOTHER KEY IS STRUCK.
)          THIS PERMITS SPECIAL CODE COMBINATIONS,
)          FOR MECHANICAL ADJUSTMENTS, TO BE TYPED.

)6.      PROGRAM DISCRPTION/THEORY OF OPERATION
)6.1      TELETYPE DIAGNOSTIC (START ADDRESS 50)
)          THIS ROUTINE WILL TEST THE TELETYPE INTER-
)          FACE LOGIC. THE ROUTINE WILL ALSO TEST
)          INPUT-OUTPUT BUS AND INTERRUPT LOGIC
)          CONTAINED IN THE CPU. AT THE END OF EACH
)          PROGRAM PASS THE WORD PASS IS PRINTED.
)          IF AFTER A FEW MINUTES THE WORD PASS IS
)          NOT PRINTED THE PROGRAM MAY BE IN A LOOP
)          OR THERE MAY BE A TELETYPE MALFUNCTION.
)6.2      PUNCH SWITCHES (START ADDRESS 51)
)          THE PUNCH SWITCHES ROUTINE PERMITS THE
)          OPERATOR TO SCOPE THE TELETYPE OUTPUT
)          LOGIC. THE ROUTINE ISSUES A I/O RESET
)          PULSE, SENDS THE CONTENTS OF THE SWITCHES
)          RIGHT HALF, WAITS 102MS, AND ITERATES THE
)          SEQUENCE.
)6.3      ECHO TEST (START ADDRESS 52)
)          THE ECHO TEST PROVIDES A MEANS OF DETER-
)          MINING IF THE CODES PRODUCED BY THE KEY-
)          BOARD ARE CORRECT. WHEN THE OPERATOR STRIKES
)          A KEY THE PROGRAM RECEIVES THE CHARACTER AND
)          CHECKS IT FOR EVEN PARITY.... THE CHARAC-
)          TOR IS STORED IN A BUFFER AND ECHOED BACK
)          TO THE OPERATOR. IF A TAB HAS BEEN TYPED
)          A RUBOUT IS ALSO ECHOED. THE OPERATOR MUST
)          CHECK THE CHARACTER PRINTED TO BE THE SAME
)          AS THE KEY DEPRESSED. WHEN THE CARRIAGE
)          RETURN KEY IS STRUCK THE PROGRAM CLEARS
)          THE BUFFER AND ECHOES A CARRIAGE RETURN
)          LINE FEED SEQUENCE. IF THE OPERATOR TERMIN-
)          ATES A LINE WITH A "CTRL Z" KEY THE PROGRAM
)          WILL INSERT A CARRIAGE RETURN AND ECHO THE
)          LINE UNTILL ANOTHER KEY IS STRUCK.

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16.4 PRINT CHARACTER SET (START ADDRESS 53)  
; A PORTION OF THE ASCII CHARACTER SET IS  
; REPEATLY TYPED.  
16.5 PUNCH AND READ RANDOM (START ADDRESS 54)  
; THIS IS THE PRIME TEST OF THE TELETYPE  
; READER AND PUNCH IN FULL DUPLEX OPERATION.  
; THE MSKO INSTRUCTION AND THE INTERRUPT SYS-  
; TEM ARE USED TO PROVIDE RANDOM START-STOP  
; SEQUENCES TO THE READER AND PUNCH. THE DATA  
; PUNCHED IS EVEN PARITY... RANDOM NUMBERS.  
16.6 PUNCH AND READ COUNT (START ADDRESS 55)  
; THIS TEST IS SIMILAR TO THE PREVIOUS TEST  
; EXCEPT THAT A COUNT PATTERN IS USED.
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17. RESTRICTIONS/MISC  
17.1 THE DIAGNOSTIC ROUTINE WILL PRINT VARIOUS  
; INDETERMINATE CHARACTORS.  
17.2 THE DIAGNOSTIC ROUTINE WILL NOT OPERATE  
; PROPERLY WITH THE MODEL 37 TELETYPE.
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.EOT

0005 .MAIN

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000001 .LOC 1
00001 000347 INTR ;INTERRUPT SERVICE ADDRESS
00002 002105 JMP @BEG ;MOTHER HEN START

000005 .LOC 5
00005 177777 XSAV: -1
00006 002007 JMP @.+1
00007 000153 START

000045 .LOC 45
00045 000310 EGGS

000050 .LOC 50
00050 002105 JMP @BEG ;DIAGNOSTIC
00051 000225 JMP SWITCH ;PRINT FROM SWITCH
00052 000240 JMP ECHO ;ECHO THE INPUT
00053 002104 JMP @ALPHA ;PRINT CHARACTER SET
00054 002102 JMP @MAINR ;RANDOM READ/PUNCH
00055 002103 JMP @MAINC ;COUNTER READ/PUNCH

00056 060010 CDVCD: 060010
00057 000010 DVCD: 10
00060 060010 C60K: 060010
00061 000153 FIRST: START
00062 002036 LAST: CRLF
00063 000000 DEVRET: 0
00064 160077 CIOT: 160077
00065 177700 M100: -100
00066 177634 CM100: -144
00067 100004 IND4: 04
00070 001762 ER: ERR
00070 006070 EHALT=JSR @ER
00071 001746 SETUP: ENTER
00072 001772 LOOP: CYCLE
00073 001734 TIME: TIMER
00074 057664 CTIML: 24500. 198 MS
00075 061634 CTIMH: 25500. 1102MS
00076 060650 CTIM: 25000. 1100MS
00077 000764 TWO: 500. 12 PER CENT OF 25000
00100 000000 TIMEX: 0
00101 000000 PASS: 0
00102 000463 MAINR: MAIN
00103 000464 MAINC: MAIN+1
00104 002044 ALPHA: ALPH
00105 000546 BEG: BEGIN
00106 000011 T94A: .TTO
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A 0206 .MAIN

00107	000000	SAV0:	0	
00110	000000	SAV1:	0	
00111	000000	SAV2:	0	
00112	000000	SAV3:	0	
00113	000000	SAVC:	0	
00114	000011	TIO:	.TTO	
00115	000010	TIOX:	.TTI	
00116	000000	TINCH:	0	
00117	135525	C1355:	135525	
00120	000000	RANI:	0	
00121	000000	BROKEN:	0	
00122	000000	ICTR:	0	
00123	000000	CRA:	0	
00124	000523	RAND:	XRAND	
00125	000000	OCTR:	0	
00126	000000	RAND:	0	
00127	000100	LT:	100	
00130	000000	TYO:	0	
00131	000000	TIMDEL:	0	
00132	000004	C4:	4	
00133	000177	C177:	177	
00134	000000	MESSR:	0	
00135	000377	C377:	377	
00136	000015	C215:	15	
00137	000012	C12:	12	
00140	004000	C4000:	4000	
00141	002342	PC5:	1250.	15 PERCENT OF 25000
00142	000347	CINTR:	INTR	
00143	002220	CTABL:	TABLE+100	
00144	002120	CTABF:	TABLE	
00145	000000	CNTR:	0	
00146	000032	C232:	32	
00147	020000	C20K:	20000	
00150	002036	ICRLF:	CRLF	
00151	002013	IMESS:	MESS	
00152	000011	C11:	11	
	000011	.TTO=11		
	000010	.TTI=10		

A 0007 .MAIN

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00153 030005 START: LDA 2,XSAV
00154 151400      INC 2,2
00155 024060      LDA 1,C60K
00156 125400      INC 1,1
00157 044056      STA 1,OVCD
00160 050106      STA 2,T04A
00161 050114      STA 2,TIO
00162 050057      STA 2,DVCD
00163 004201      JSR DEVC0      ;CHANGE THE .TIO DEVICE CODES

00164 024060      LDA 1,C60K
00165 044056      STA 1,OVCD
00166 030005      LDA 2,XSAV
00167 050115      STA 2,TT0X
00170 050057      STA 2,DVCD
00171 004201      JSR DEVC0      ;CHANGE THE .TTT DEVICE CODES

00172 024065      LDA 1,M100      ;CHANGE CONSTANTS FOR THE NEXT RUN.
00173 020060      LDA 0,C60K
00174 123400      AND 1,0
00175 024057      LDA 1,DVCD
00176 123000      ADD 1,0
00177 040060      STA 0,C60K
00200 063077      HALT

00201 054063 DEVC0: STA 3,DEVRET
00202 030061      LDA 2,FIRST
00203 021000      LDA 0,0,2      ;FIX THE DEVICE CODE
00204 024064      LDA 1,CIOT      ;160077
00205 123400      AND 1,0
00206 024056      LDA 1,OVCD      ;INITIALLY 060010
00207 105415      SUB# 0,1,SNR
00210 000216      JMP DEVC2
00211 151400 DEVC1: INC 2,2
00212 020062      LDA 0,LAST
00213 112414      SUB# 0,2,SZR
00214 000203      JMP DEVC0+2
00215 000063      JMP 0DEVRET
00216 021000 DEVC2: LDA 0,0,2      ;CHANGE THE INSTRUCTION
00217 024065      LDA 1,M100
00220 123400      AND 1,0      ;MASK THE DEVICE CODE
00221 024057      LDA 1,DVCD
00222 123000      ADD 1,0      ;NEW DEVICE CODE
00223 041000      STA 0,0,2
00224 000211      JMP DEVC1

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A DUMP .MAIN

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00225 062677 SWITCH: IORST           ;TYPE FROM SWITCHES
00226 020075          LDA 0,CTIMH
00227 100420          NEGZ 0,0
00230 102411          SUB# 7,0,SKP
00231 102410          SUB# 0,0
00232 063600          SKPDN 0           ;DELAY ABOUT 100 MS
00233 101404          INC 0,0,SZR
00234 000230          JMP .-4
00235 070477          READS 3
00236 075111          DOAS 3,.TTO       ;SEND C(SWITCH) TO
00237 000226          JMP SWITCH+1     ;THE TELETYPE.

00240 006150 ECHO:   JSR @ICRLF       ;ECHO ON OUTPUT THE
00241 030143          LDA 2,CTARL       ;CHARACTERS RECEIVED
00242 034144          LDA 3,CTARF       ;ON INPUT.
00243 102400          SUB 0,0
00244 041400          STA 0,0,3
00245 175400          INC 3,3
00246 156414          SUB# 2,3,SZR
00247 000244          JMP .-3
00250 030144          LDA 2,CTARF
00251 040145          STA 0,CNTR       ;SET COUNTER TO 0
00252 004314 ECHO1:  JSR TIN           ;LOOK FOR INPUT
00253 024135          LDA 1,C215
00254 106405          SUB 0,1,SNR
00255 000240          JMP ECHO         ;CARRIAGE TYPED
00256 124146          LDA 1,C232
00257 106414          SUB# 0,1,SZR
00260 000267          JMP ECHO3       ;STORE THE CHARACTER
00261 006150 ECHO2:  JSR @ICRLF       ;CNTRL Z TYPED
00262 063710          SKPDZ .TTI       ;KEEP SENDING THE
00263 000306          JMP ECHO4       ;LINE TYPED UNTILL
00264 006151          JSR @IMFSS       ;A KEY IS STRUCK.
00265 002120          TABLE
00266 000261          JMP ECHO2
00267 024135 ECHO3:  LDA 1,C377       ;STORE THE CHARACTER
00270 030145          LDA 2,CNTR
00271 010145          ISZ CNTR
00272 034144          LDA 3,CTARF       ;BEGIN OF TABLE
00273 151223          MOVZR 2,2,SNR
00274 123401          AND 1,0,SKP
00275 123720          ANDS 1,0
00276 157000          ADD 2,3
00277 025400          LDA 1,0,3
00300 107000          ADD 0,1
00301 045400          STA 1,0,3
00302 030143          LDA 2,CTARL
00303 156414          SUB# 2,3,SZR       ;CHECK FOR
00304 000252          JMP ECHO1       ;BUFFER FULL
00305 000240          JMP ECHO
00306 060210 ECHO4:  NIOC .TTI
00307 000240          JMP ECHO
00310 000000 EGGS:   0           ;HEN FLAG
00311 000000          0           ;DEVICE CODE
00312 000000          0           ;ASR37 FLAG
00313 000000          0           ;PASS COUNTER

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A 0000 .MAIN

00314	063610	TIN:	SKPON .TTI	:INPUT A CHARACTER
00315	000314		JMP .-1	:FROM THE KEYBOARD.
00316	060610		DIAC 0,.TTI	:CHAR TO ACC
00317	063710		SKPOZ .TTI	
00320	063077		HALT	:FLAG CHECK!
00321	061111		DIAS 0,.TTO	:XMIT CHARACTER
00322	111020		MOVZ 0,0	
00323	126000		ADC 1,1	
00324	147000		ADD 2,1	
00325	133404		AND 1,2,SZR	
00326	000323		JMP .-3	
00327	024147		LDA 1,C20K	:LOOK AT SWITCH 2
00330	070477		READS 2	
00331	133404		AND 1,2,SZR	
00332	000335		JMP .+3	:DON'T CHECK PARITY.
00333	101012		MOV# 0,0,SZC	:C(0)=CHARACTER
00334	063077		HALT	
00335	024133		LDA 1,C177	:STRIP THE PARITY BIT.
00336	123400		AND 1,0	
00337	024152		LDA 1,C11	
00340	106414		SUB# 0,1,SZR	:CHECK FOR TAB
00341	001400		JMP 0,3	:EXIT NOT A TAB.
00342	126000		ADC 1,1	
00343	063611		SKPON .TTO	:INSERT A RETURN
00344	000343		JMP .-1	
00345	065111		DIAS 1,.TTO	
00346	001400		JMP 0,3	
00347	040107	INTR:	STA 0,SAV0	:INTERRUPT SERVICE
00350	044110		STA 1,SAV1	
00351	050111		STA 2,SAV2	
00352	054112		STA 3,SAV3	
00353	175200		MOVR 3,3	
00354	054113		STA 3,SAV3	:SAVE AC+CARRY.
00355	063777		SKPOZ CPU	
00356	063077		HALT	:POWER FAIL FLAG?
00357	024114		LDA 1,TIO	
00360	071477		INTA 2	
00361	132415		SUB# 1,2,SNR	
00362	000447		JMP TYPE	:OUTPUT
00363	024115		LDA 1,TIOX	
00364	132414		SUB# 1,2,SZR	
00365	063077		HALT	:UNKNOWN INTERRUPT! SEE C(2)
00366	063610	XTIN:	SKPON .TTI	:READER INPUT
00367	063077		HALT	:RD DONE FLAG FAILED.
00370	064510		DIAS 1,.TTI	:GET CHARACTER
00371	063710		SKPOZ .TTI	
00372	063077		HALT	:FLAG CHECK
00373	063410		SKPON .TTI	
00374	063077		HALT	:FLAG CHECK
00375	044116		STA 1,TINCH	:SAVE INPUT CHARACTER.

00376	125004	TIN1:	MOV 1,1,SZR	
00377	000410		JMP TIN2	
00400	020117		LDA 0,C1355	NON ZERO INPUT
00401	040120		STA 0,RANI	INIT RANDOM NUMBER
00402	010121		ISZ BROKEN	IF TOO MUCH LEADER
00403	102431		SUB 0,0,SKP	FOR ALL CHARACTORS 0
00404	063077		HALT	ERROR.
00405	040122		STA 0,ICTR	INIT COUNT PATTERN.
00406	000535		JMP DISMIS	EXIT INTERRUPT.
00407	030066	TIN2:	LDA 2,C1100	
00410	050121		STA 2,BROKEN	
00411	034123		LDA 3,CRA	
00412	175005		MOV 3,3,SNR	
00413	000410		JMP TIN3	RANDOM DATA
00414	010122		ISZ ICTR	COUNTER
00415	101010		MOV# 0,0	
00416	020122		LDA 0,ICTR	
00417	106414		SUB# 0,1,SZR	C(0)=GOOD,C(1)=BAD
00420	063077		HALT	READ OR PUNCH ERROR.
00421	044122		STA 1,ICTR	RESET COUNTER
00422	000471		JMP DISMIS	EXIT INTERRUPT.
00423	006124	TIN3:	JSR @RAND	
00424	000120		RANI	
00425	024116		LDA 1,TINCH	
00426	106414		SUB# 0,1,SZR	C(0)=GOOD,C(1)=BAD
00427	063077		HALT	READ OR PUNCH ERROR.
00430	000463		JMP DISMIS	
00431	063611	TYPE:	SKPON .TTO	OUTPUT COUNT OR RANDOM
00432	063077		HALT	PATTERN. FLAG CHECK.
00433	034123		LDA 3,CRA	SWITCH
00434	175005		MOV 3,3,SNR	
00435	000405		JMP TYPE1	RANDOM
00436	010125		ISZ OCTR	INC OUT COUNTER
00437	101010		MOV# 0,0	
00440	020125		LDA 0,OCTR	
00441	000403		JMP .+3	
00442	006124	TYPE1:	JSR @RAND	
00443	000126		RAND	
00444	024127		LDA 1,LT	LEADER CHECK
00445	030130		LDA 2,TYO	
00446	132032		ADCZ# 1,2,S7C	
00447	000406		JMP TYPE2	
00450	020117		LDA 0,C1355	RESET RANDOM IN LEADER
00451	040126		STA 0,RAND	
00452	010130		ISZ TYO	+1 TO LEADER COUNT
00453	102400		SUB 0,0	
00454	040125		STA 0,OCTR	
00455	061111	TYPE2:	DOAS 0,.TTO	SEND THE CHARACTER
00456	063411		SKPRN .TTO	
00457	063077		HALT	FLAG CHECK
00460	063711		SKPOZ .TTO	
00461	063077		HALT	FLAG CHECK
00462	000431		JMP DISMIS	EXIT INTERRUPT.

A 0011 .MAIN

00463	102400	MAIN:	SUB 0,0,SKP	IREAD/PUNCH RANDOM
00464	102000		ADC 0,0	IREAD/PUNCH COUNTER
00465	040123		STA 0,CRA	INITIALIZE
00466	062677		IORST	
00467	020056		LDA 0,CM100	
00470	040121		STA 0,BROKEN	
00471	102400		SUB 0,0	
00472	040130		STA 0,TYO	
00473	024142		LDA 1,CINTR	
00474	044001		STA 1,1	
00475	060177		INTEN	ENABLE INTERRUPTS
00476	061111		DIAS 0,.TT0	
00477	060510		DIAS 0,.TTI	
00500	006124	MAIN1:	JSR 0RANO	MAIN LOOP CNTL
00501	000131		TIMDEL	OF READER/PUNCH
00502	152400		SUB 2,2	INTERRUPT.
00503	104300		COMS 0,1	
00504	072077		MSK0 0	
00505	125424		INC 1,1,S7R	
00506	000777		JMP .-1	
00507	151400		INC 2,2	
00510	151237		MOVZR# 0,2,SNR	
00511	000772		JMP MAIN1+3	
00512	000766		JMP MAIN1	
00513	020107	DISMIS:	LDA 0,SAV0	DISMISS A INTERRUPT
00514	024110		LDA 1,SAV1	RESTORE MACHINE STATE.
00515	030111		LDA 2,SAV2	
00516	034113		LDA 3,SAV3	
00517	175100		MOVL 3,3	
00520	034112		LDA 3,SAV3	
00521	060177		INTEN	
00522	002000		JMP 00	
00523	027400	XRAND:	LDA 1,00,3	GENERATE A 8 BIT
00524	020133		LDA 0,0177	EVEN PARITY RANDOM
00525	131120		MOVZL 1,2	NUMBER.
00526	151120		MOVZL 2,2	
00527	133000		ADD 1,2	
00530	024403		LDA 1,.,+3	
00531	147000		ADD 2,1	
00532	101300		MOVS 0,0	
00533	123725		ANDZS 1,0,SNR	
00534	000770		JMP XRAND+1	
00535	047400		STA 1,00,3	STORE A 16 BIT RANDOM
00536	111300		MOVS 0,2	
00537	126000		ADC 1,1	PARITY GENERATOR
00540	107000		ADD 0,1	
00541	123404		AND 1,0,S7R	
00542	000775		JMP .-3	
00543	101200		MOVR 0,0	
00544	143300		ADDS 2,2	IC(0)R=EVEN PARITY#
00545	001401		JMP 1,3	

00546 102400	BEGIN:	SUB 0,0	;DIAGNOSTIC STARTS HERE.
00547 040101		STA 0,PASS	
00550 102622	T00:	SUBZR 0,0	;INIT FOR POSSIBLE INTERRUPT.
00551 040001		STA 0,1	
00552 006071		JSR @SETUP	;THE SELB LINE IS
00553 063520		SKPRZ 0	;GROUNDED.
00554 006070		EHALT	;CHECK (AR2)
00555 006072		JSR @LOOP	;AND INPUT TO SKIP LOGIC.
00556 006071	T02:	JSR @SETUP	;THE SELD LINE IS
00557 063700		SKPDZ 0	;GROUNDED.
00560 006070		EHALT	;CHECK (AR0).
00561 006072		JSR @LOOP	;CHECK SKIP LOGIC.
00562 006071	T04:	JSR @SETUP	;TTO SHOULD NOT BE
00563 060211		NTOC .TTO	;BUSY,ITS BEEN CLEARED.
00564 062677		IORST	;IN TO DIFFERENT WAYS.
00565 063511		SKPRZ .TTO	;CHECK TTO BUSY(1) INPUT
00566 006070		EHALT	;TO SELB LINE (AR2).
00567 006072		JSR @LOOP	
00570 006071	T06:	JSR @SETUP	;TROUBLE IN CPU.
00571 063411		SKPRN .TTO	;SELECTION OF SKIP SEV.
00572 101001		MOV 0,0,SKP	;CHECK SKIP INVERT LOGIC
00573 006070		EHALT	
00574 006072		JSR @LOOP	
00575 006071	T08:	JSR @SETUP	;TTT SHOULD NOT BE BUSY
00576 060210		NTOC .TTI	;TTT HAS BEEN CLEARED IN
00577 062677		IORST	;TWO WAYS. CHECK TTT INPUT
00580 063510		SKPRZ .TTI	;TO SELB LINE (AR2).
00581 006070		EHALT	;CHECK THE FLOP.
00582 006072		JSR @LOOP	
00603 006071	T10:	JSR @SETUP	;TTO SHOULD NOT BE DONE
00604 060211		NTOC .TTO	;TTT HAS BEEN CLEARED
00605 062677		IORST	;IN TWO WAYS. CHECK
00606 063711		SKPDZ .TTO	;TTO INPUT TO SELD (AR0)
00607 006070		EHALT	;ALSO THE TTO DONE FLOP.
00610 006072		JSR @LOOP	
00611 006071	T12:	JSR @SETUP	;TTT SHOULD NOT BE DONE
00612 060210		NTOC .TTI	;TTT HAS BEEN CLEARED
00613 062677		IORST	;IN TWO WAYS. CHECK
00614 063710		SKPDZ .TTI	;TTT INPUT TO SELD (AR0).
00615 006070		EHALT	;ALSO THE TTI DONE FLOP.
00616 006072		JSR @LOOP	

A 0013 .MAIN

00617	006071	T26:	JSR @SETUP	;THE TTI BUSY FLOP
00620	060110		NIOS .TTI	;WAS NOT CLEARED BY
00621	060210		NIOC .TTI	;A CLEAR PULSE OR
00622	062677		IORST	;I/O RESET, CHECK CLEAR
00623	063510		SKPBZ .TTI	;INPUT TO FLOP, ALSO
00624	006070		EHALT	;E39 R=9, E41 R=9-10
00625	006072		JSR @LOOP	
00626	006071	T28:	JSR @SETUP	;THE TTI BUSY FLOP
00627	060110		NIOS .TTI	;WAS NOT CLEARED BY
00630	062677		IORST	;I/O RESET, CHECK
00631	063510		SKPBZ .TTI	;E41 R=9, I/O RESET
00632	006070		EHALT	;INPUT TO CLEAR SIDE
00633	006072		JSR @LOOP	;OF TTI BUSY.
00634	006071	T30:	JSR @SETUP	;THE TTI BUSY FLOP
00635	060110		NIOS .TTI	;WAS NOT CLEARED BY
00636	060210		NIOC .TTI	;TTI SELECT AND A
00637	063510		SKPBZ .TTI	;CLEAR PULSE, CHECK
00640	006070		EHALT	;AND GATE E42 8-9-10
00641	006072		JSR @LOOP	;AND THE INPUT TO E41.
00642	006071	T32:	JSR @SETUP	;SELECTING THE TTI
00643	060010		NIO .TTI	;WITHOUT A START
00644	063510		SKPBZ .TTI	;PULSE SET TTI
00645	006070		EHALT	;RDP BUSY, CHECK
00646	006072		JSR @LOOP	;E42 11-12-13
00647	020426		LDA 0, K40	;A TEST THE INSURE THAT THE
00650	040426		STA 0, XORDEV	;TTT RESPONDS ONLY TO ONE
00651	006071	T36:	JSR @SETUP	;DEVICE CODE.
00652	060111		NIOS .TTO	
00653	006073		JSR @TIME	
00654	063511		SKPBZ .TTO	
00655	060111		NIOS .TTO	
00656	020421		LDA 0, CNIOC	;CNIOC .TTO
00657	024417		LDA 1, XORDEV	
00660	131000		MOV 1, 2	
00661	113520		ANDZL 0, 2	
00662	107000		ADD 0, 1	;C(1)=A NIOC INSTRUCTION
00663	146400		SUB 2, 1	;TO A DEVICE OTHER THAN
00664	044401		STA 1, .+1	;THE .TTO, IF THE .TTO
00665	000000		0	;RESPONDS CHECK THE DEVICE
00666	063411		SKPBN .TTO	;SELECTION LOGIC AND
00667	006070		EHALT	;JUMPPERS.
00670	006072		JSR @LOOP	
00671	020405		LDA 0, XORDEV	
00672	101224		MOVZR 0, 0, SZR	
00673	000755		JMP T36-1	

A 0014 .MAIN

00674	000404		JMP .+4	
00675	000040	K401	40	
00676	000000	XORDEV:	0	
00677	060211	CNIOC:	NIOC .TTO	
00700	006071	T54:	JSR @SETUP	!START AND TTO SELECT
00701	060111		NIOS .TTO	!FAILED TO SET TTO
00702	063411		SKPBN .TTO	!BUSY. CHECK SELB (A92)
00703	006070		EHALT	!BUSY FLOP, ETC,ETC.
00704	006072		JSR @LOOP	
00705	006071	T56:	JSR @SETUP	!THE BUSY FLOP (TTO)
00706	060111		NIOS .TTO	!WAS GATED ON TO THE
00707	063500		SKPBZ 0	!SELB LINE (A92)
00710	006070		EHALT	!WITHOUT TTO SELECT
00711	006072		JSR @LOOP	!BEING PRESENT.
00712	006071	T58:	JSR @SETUP	!THE TTO BUSY FLOP
00713	060111		NIOS .TTO	!WAS CLEARED VIA A
00714	060200		NIOC 0	!START PULSE WITH-
00715	063411		SKPBN .TTO	!OUT TTO SELECT.
00716	006070		EHALT	
00717	006072		JSR @LOOP	

A 0015 .MAIN

00720	006071	T60:	JSR @SETUP	THE TTD BUSY FLOP
00721	060111		NIOS .TTD	WAS CLEARED VIA
00722	060011		NIO .TTD	SELECTING THE TTD
00723	063411		SKPBN .TTD	WITHOUT A CLEAR PULSE.
00724	006070		EHALT	
00725	006072		JSR @LOOP	
00726	006071	T64:	JSR @SETUP	THE TTI BUSY FLOP
00727	060110		NIOS .TTI	FAILED TO SET VIA
00730	063410		SKPBN .TTI	A START PULSE.
00731	006070		EHALT	CHECK THE READER
00732	006073		JSR @TIME	START LEVEL, ETC, ETC.
00733	063510		SKPBZ .TTI	
00734	006072		JSR @LOOP	
00735	006071	T68:	JSR @SETUP	SETTING TTI BUSY
00736	060110		NIOS .TTI	CAUSED SELA
00737	063500		SKPBZ 0	LINE (A82) TO GO LOW
00740	006070		EHALT	WITHOUT TTI SELECT.
00741	006073		JSR @TIME	CHECK AND GATE OF
00742	063510		SKPRZ .TTI	(TTI RDR BUSY(1),
00743	006072		JSR @LOOP	TTI SELECT). E29 1-2-3
00744	006071	T68:	JSR @SETUP	SETTING TTD DONE
00745	060111		NIOS .TTD	CAUSED ALL DONE
00746	006073		JSR @TIME	FLAGS TESTING TO SKIP.
00747	063511		SKPBZ .TTD	DONE IS GATED ONTO
00750	063700		SKPDZ 0	SEL0 (A80) WITHOUT
00751	006070		EHALT	DEVICE SELECT.
00752	006072		JSR @LOOP	
00753	006071	T70:	JSR @SETUP	BOTH A I/O RESET
00754	060111		NIOS .TTD	AND A CLEAR PULSE
00755	006073		JSR @TIME	FAILED TO CLEAR
00756	063511		SKPBZ .TTD	THE DONE FLAG(TTD).
00757	060211		NIOC .TTD	CHECK THE START+CLR+RST
00760	062677		IORST	PULSE, THE FOLLOWING
00761	063711		SKPDZ .TTD	INVERTER, THE
00762	006070		EHALT	DONE FLOP ITSELF.
00763	006072		JSR @LOOP	
00764	006071	T72:	JSR @SETUP	A I/O RESET FAILED
00765	060111		NIOS .TTD	TO CLEAR THE TTD
00766	006073		JSR @TIME	DONE FLOP. INPUT
00767	063511		SKPRZ .TTD	TO OR GATE E40 FAILED
00770	062677		IORST	CHECK LEAD 10, THEN
00771	063711		SKPDZ .TTD	REPLACE E40.
00772	006070		EHALT	
00773	006072		JSR @LOOP	



A 0016 .MAIN

00774 006071 T74:	JSR @SETUP	;A TTO CLEAR PULSE
00775 060111	NIOS .TTO	;FAILED TO RESET
00776 006073	JSR @TIME	;TTO DONE, CHECK
00777 063511	SKPBZ .TTO	;F40=9,THE OR GATE
01000 060211	NIOC .TTO	;THAT PRODUCES THE
01001 063711	SKPDZ .TTO	; (STRT+CLR+RST) PULSE.
01002 006070	EHALT	
01003 006072	JSR @LOOP	
01004 006071 T76:	JSR @SETUP	;A TTO STRT PULSE
01005 060111	NIOS .TTO	;FAILED TO CLEAR TTO
01006 006073	JSR @TIME	;DONE. CHECK F40=11
01007 063511	SKPBZ .TTO	;THE OR GATE THAT
01010 060111	NIOS .TTO	;PRODUCES THE (STRT
01011 063711	SKPDZ .TTO	;+CLR+RST) PULSE.
01012 006070	EHALT	
01013 006072	JSR @LOOP	
01014 006071 T78:	JSR @SETUP	;THE TTO FINISH PULSE.
01015 102000	ADC 0,0	;FAILED TO CLEAR BUSY.
01016 061111	DDAS 0,.TTO	;SCOPE TO FIND OUT WHY.
01017 006073	JSR @TIME	
01020 063511	SKPBZ .TTO	
01021 063511	SKPBZ .TTO	;A LARGE NUMBER OF THINGS
01022 006070	EHALT	;COULD HAVE FAILED.
01023 006072	JSR @LOOP	
01024 006071 T80:	JSR @SETUP	;TRY TO SET DONE
01025 102000	ADC 0,0	;VIA TTO FINISH. CHECK
01026 061111	DDAS 0,.TTO	;SELD (A80)
01027 006073	JSR @TIME	;INPUTS AND
01030 063511	SKPBZ .TTO	;THE TTO DONE FLOP
01031 063611	SKPDZ .TTO	;OUTPUT,RESET,AND
01032 006070	EHALT	;CLOCK INPUTS.
01033 006072	JSR @LOOP	
01034 006071 T82:	JSR @SETUP	;THE CPU FAILED
01035 102000	ADC 0,0	;TO READ IN ANYTHING
01036 061477	INTA 0	;ON INTA, CHECK CPU.
01037 100015	COM# 0,0,SNR	
01040 006070	EHALT	
01041 006072	JSR @LOOP	
01042 006071 T84:	JSR @SETUP	;A BIT 15 WAS READ
01043 102520	SUBZL 0,0	;IN ON INTA.
01044 065477	INTA 1	
01045 107414	AND# 0,1,SZR	
01046 006070	EHALT	
01047 006072	JSR @LOOP	

01050	006071	T86:	JSR @SETUP	IAFTER A I/O RESET
01051	061477		INTA 0	I(ISSUED BY SETUP) INTA
01052	101004		MOV 0,0,SZR	ISHOULD READ BACK NO
01053	006070		EHALT	IBITS, BUT IT DID
01054	006072		JSR @LOOP	IGET BITS BACK.
01055	006071	T88:	JSR @SETUP	ISET THE TTO
01056	102000		ADC 0,0	IDONE FLAG, THEN
01057	062077		MSKO 0	Iperform a INTA
01060	062677		IDRST	IAND CHECK IF
01061	061111		DOAS 0,.TTO	IANY BITS WERE
01062	006073		JSR @TIME	Ireceived BY THE
01063	063511		SKPBZ .TTO	ICPU.
01064	061477		INTA 0	ISUGGEST TTO INT
01065	101005		MOV 0,0,SNR	Irequest FLOP FAILED.
01066	006070		EHALT	
01067	006072		JSR @LOOP	
01070	006071	T90:	JSR @SETUP	IWITH THE DONE
01071	060111		NIOS .TTO	IFLAG SET INTA
01072	006073		JSR @TIME	ISHOULD READ BACK
01073	063511		SKPBZ .TTO	IBIT 15. CHECK
01074	061477		INTA 0	IINTA INPUT TO BIT 15
01075	126520		SUBZL 1,1	IF31,F32,E12
01076	107405		AND 0,1,SNR	
01077	006070		EHALT	
01100	006072		JSR @LOOP	
01101	006071	T92:	JSR @SETUP	IA MSKO WITH BIT 15
01102	060111		NIOS .TTO	IZERO SHOULD NOT
01103	006073		JSR @TIME	ICHANGE THE STATE OF
01104	063511		SKPBZ .TTO	ITTO INT DISABLT.
01105	102120		ADCZL 0,0	IND BITS WERE READ
01106	062077		MSKO 0	IRACK ON INTA HOW-
01107	061477		INTA 0	IFEVER, CHECK DATA-15
01110	101005		MOV 0,0,SNR	IINPUT TO TTO INT
01111	006070		EHALT	IDISABLE FLOP.
01112	006072		JSR @LOOP	
01113	006071	T94:	JSR @SETUP	Ithe DEVICE CODE
01114	060111		NIOS .TTO	IREAD BACK FOR
01115	006073		JSR @TIME	Ithe TTO SHOULD
01116	063511		SKPBZ .TTO	IRE 11 (FIRST TTO) OR
01117	061477		INTA 0	I51 (SECOND TTO).
01120	024106		LDA 1,T94A	ISOME OTHER NUMBER
01121	106414		SUB# 0,1,SZR	Iwas READ BACK.
01122	006070		EHALT	IEXAMINE C(0).
01123	006072		JSR @LOOP	

01124 006071 T06:  
 01125 102520  
 01126 062077  
 01127 062677  
 01130 060111  
 01131 006073  
 01132 063511  
 01133 061477  
 01134 101005  
 01135 006070  
 01136 006072

JSR @SETUP  
 SUBZL 0,0  
 MSKO 0  
 IORST  
 NIOS ,TTO  
 JSR @TIME  
 SKPBZ ,TTO  
 INTA 0  
 MOV 0,0,SNR  
 EHALT  
 JSR @LOOP

;THE TTO INT DISABLE  
 ;FLOP IS SET VIA  
 ;MSKO THEN CLEARED  
 ;VIA I/O RESET. NO  
 ;BITS WERE READ BACK.  
 ;SUGGEST I/O RESET  
 ;FAILED TO CLEAR  
 ;TTO INT DISABLE FLOP.  
 ;FOR ITS INPUT TO TTO  
 ;INT REQ FAILED.

01137 006071 T08:  
 01140 102520  
 01141 062077  
 01142 060111  
 01143 006073  
 01144 063511  
 01145 061477  
 01146 101004  
 01147 006070  
 01150 006072

JSR @SETUP  
 SUBZL 0,0  
 MSKO 0  
 NIOS ,TTO  
 JSR @TIME  
 SKPBZ ,TTO  
 INTA 0  
 MOV 0,0,SZR  
 EHALT  
 JSR @LOOP

;THE TTO INT DISABLE  
 ;FLOP EITHER FAILED TO  
 ;SET VIA MSKO OR  
 ;THE AND GATE  
 ;TTO DONE(1),TTO INT DIS(0)  
 ;FAILED, BECAUSE IT  
 ;DID NOT PREVENT  
 ;TTO INT REQ FROM  
 ;SETTING.

01151 006071 U00:  
 01152 102000  
 01153 061111  
 01154 006073  
 01155 063511  
 01156 102000  
 01157 061111  
 01160 006073  
 01161 063511  
 01162 024074  
 01163 106432  
 01164 006070  
 01165 006072

JSR @SETUP  
 ADC 0,0  
 DOAS 0,.TTO  
 JSR @TIME  
 SKPBZ ,TTO  
 ADC 0,0  
 DOAS 0,.TTO  
 JSR @TIME  
 SKPBZ ,TTO  
 LDA 1,CTIML  
 SUBZ# 0,1,S7C  
 EHALT  
 JSR @LOOP

;CHECK THE TIME IT TAKES  
 ;TO PRINT A CHARACTER. CHECK  
 ;AND GATE TO MAKE UP FINISH  
 ;IF TIME IS 0MS,STOP(1)  
 ;" " " 18MS,"TTO(1)  
 ;" " " 27MS," 1(1)  
 ;" " " 36MS," 2(1)  
 ;" " " 45MS," 3(1)  
 ;" " " 54MS," 4(1)  
 ;" " " 63MS," 5(1)  
 ;" " " 72MS," 6(1)  
 ;" " " 81MS," 7(1)  
 ;" " " 90MS," 8(1)

01166 006071 U02:  
 01167 102000  
 01170 061111  
 01171 006073  
 01172 063511  
 01173 102000  
 01174 061111  
 01175 061011  
 01176 061011  
 01177 061011  
 01200 006073  
 01201 063511  
 01202 024074  
 01203 106432  
 01204 006070  
 01205 006072

JSR @SETUP  
 ADC 0,0  
 DOAS 0,.TTO  
 JSR @TIME  
 SKPBZ ,TTO  
 ADC 0,0  
 DOAS 0,.TTO  
 DOA 0,.TTO  
 DOA 0,.TTO  
 DOA 0,.TTO  
 JSR @TIME  
 SKPBZ ,TTO  
 LDA 1,CTIML  
 SUBZ# 0,1,S7C  
 EHALT  
 JSR @LOOP

;A CHECK TO INSURE THAT  
 ;DOA TO THE TTO DOES  
 ;NOT SHIFT THE REGISTER  
 ;IF TTO IS BUSY  
 ;CHECK AND GATE OF  
 ;DATA OUT-A,TTO SELECT.  
 ;TTO BUSY(0) THE TTO  
 ;BUSY 0 INPUT.

;FAILURE IF TIME SHORT.  
 ;C(CTIML)=90% OF TOTAL.

A 0019 .MAIN

01206	006071	U04:	JSR @SETUP	;TEST THE TIME FOR TO
01207	102000		ADC 0,0	;LONG. CHECK AND GATE
01210	061111		DOAS 0, .TTO	; (DATA OUTA, TTO SELECT,
01211	006073		JSR @TIME	; TTO BUSY (0)).
01212	063511		SKPBZ .TTO	
01213	102000		ADC 0,0	
01214	061111		DOAS 0, .TTO	
01215	006073		JSR @TIME	
01216	063511		SKPBZ .TTO	
01217	024075		LDA 1, CTIMH	
01220	106433		SUBZ# 0, 1, SNC	
01221	006070		EHALT	
01222	006072		JSR @LOOP	
01223	006071	U06:	JSR @SETUP	;TEST FOR SHORT TIME
01224	061111		DOAS 0, .TTO	;IF START FAILED TO
01225	006073		JSR @TIME	;CLEAR OUTPUT BUFFER
01226	063511		SKPBZ .TTO	;IT WILL CONTAIN ALL
01227	060111		NTOS .TTO	;ONES. CHARACTER WILL
01230	006073		JSR @TIME	;END QUICKLY.
01231	063511		SKPBZ .TTO	
01232	024074		LDA 1, CTIML	
01233	106432		SUBZ# 0, 1, SZC	
01234	006070		EHALT	
01235	006072		JSR @LOOP	
01236	006071	U10:	JSR @SETUP	;TEST FOR LONG TIME
01237	061111		DOAS 0, .TTO	;IF STOP1 WAS 0 AND
01240	006073		JSR @TIME	;START STILL GOT THROUGH
01241	063511		SKPBZ .TTO	;THE AND GATE TO
01242	061011		DOA 0, .TTO	;RESET THE BUFFER
01243	060111		NTOS .TTO	;THE TIME WOULD BE
01244	006073		JSR @TIME	;LONG.
01245	063511		SKPBZ .TTO	
01246	024075		LDA 1, CTIMH	
01247	106433		SUBZ# 0, 1, SNC	
01250	006070		EHALT	
01251	006072		JSR @LOOP	

A 0020 .MAIN

01252 006071 U14:	JSR #SETUP	TIME TO PRINT A
01253 061111	DOAS 0,.TTO	CHARACTER IS LESS
01254 006073	JSR #TIME	THAN 2% OFF FROM 100MS.
01255 063511	SKPRZ .TTO	FOR A ERROR IS ASSUMED.
01256 061111	DOAS 0,.TTO	
01257 006073	JSR #TIME	
01260 063511	SKPBZ .TTO	
01261 024076	LDA 1,CTIM	
01262 106423	SUBZ 0,1,SNC	
01263 124400	NEG 1,1	
01264 030077	LDA 2,TWO	
01265 132433	SUBZ# 1,2,SNC	
01266 006070	EHALT	
01267 006072	JSR #LOOP	

01270 006071 U16:	JSR #SETUP	THE MSKO LEVEL
01271 102520	SUBZL 0,0	WAS RAISED AT DT0B
01272 062000	DOB 0,0	TIME EVEN THOUGH
01273 060111	NIOS .TTO	IT WAS NOT A CPU INST.
01274 006073	JSR #TIME	CHECK MSKO (A30).
01275 063511	SKPBZ .TTO	
01276 061477	INTA 0	
01277 101005	MOV 0,0,SNR	
01300 006070	EHALT	
01301 006072	JSR #LOOP	

01302 006071 U18:	JSR #SETUP	THE MSKO LEVEL
01303 102520	SUBZL 0,0	WAS RAISED AT
01304 060077	NT0 CPU	CPU INST TIME
01305 060111	NIOS .TTO	EVEN THOUGH NO
01306 006073	JSR #TIME	DT0B WAS GIVEN.
01307 063511	SKPBZ .TTO	
01310 061477	INTA 0	
01311 101005	MOV 0,0,SNR	
01312 006070	EHALT	
01313 006072	JSR #LOOP	

01314 006071 U20:	JSR #SETUP	THE INTA LEVEL (A40)
01315 060111	NIOS .TTO	WAS RAISED AT DT0B
01316 006073	JSR #TIME	TIME EVEN THOUGH NO
01317 063511	SKPRZ .TTO	CPU INSTRUCTION WAS
01320 061400	DIB 0,0	GIVEN.
01321 024405	LDA 1,U20A	
01322 106415	SUB# 0,1,SNR	
01323 006070	EHALT	
01324 006072	JSR #LOOP	
01325 101011	MOV# 0,0,SKP	
01326 000011 U20A:	.TTO	

.EOT

0021 .MAIN

01327	006071	B10:	JSR @SETUP	IGROUND THE INTR LINE.
01330	102620		SUBZR 0,0	IND INTERRUPT SHOULD
01331	040000		STA 0,0	OCURE BECAUSE IND
01332	040001		STA 0,1	IS NOT SET.
01333	061111		DOAS 0,.TTO	
01334	063511		SKPBZ .TTO	ICHECK SET GATE ON
01335	000777		JMP .-1 IINTR0.	TON SHOULD BE OFF
01336	024000		LDA 1,0	
01337	106414		SUB# 0,1,SZR	
01340	006070		EHALT	
01341	102000		ADC 0,0	
01342	006072		JSR @LOOP	
01343	006071	B11:	JSR @SETUP	IGROUND THE INTR LINE.
01344	102620		SUBZR 0,0	ITURN THE INTERRUPT
01345	040000		STA 0,0	SYSTEM ON AND OFF.
01346	040001		STA 0,1	IND INTERRUPT SHOULD
01347	061111		DOAS 0,.TTO	OCUR.
01350	063511		SKPBZ .TTO	ICHECK INTR0 AND
01351	000777		JMP .-1	IPTG2 SET GATE
01352	060177		NIOS CPU	
01353	060277		NIOC CPU	
01354	024000		LDA 1,0	
01355	106414		SUB# 0,1,SZR	
01356	006070	B11A:	EHALT	
01357	006072		JSR @LOOP	
01360	006071	B12:	JSR @SETUP	IATTEMPT PI-DEFER SEQUENCE
01361	152001		ADC 2,2,SKP	
01362	001375		B12A	IINTERRUPT RETURN ADDR
01363	020777		LDA 0,.-1	
01364	040001		STA 0,1	
01365	024407		LDA 1,B12A-1	
01366	044002		STA 1,2	IHALT IN LOC 2
01367	071111		DOAS 2,.TTO	
01370	063611		SKPON .TTO	IGROUND THE INTR LINE
01371	000777		JMP .-1	
01372	060177		NIOS CPU	ITON
01373	176400		SUB 3,3	IND INTERRUPT. CHECK
01374	006070		EHALT	IINTR0,PI OR INTERRUPT
01375	006072	B12A:	JSR @LOOP	IDEFERRED THRU 0 NOT 1
				ICHECK "PLUS ONE" FOR MA=1
				IIN DEFER CYCLE

01376	006071	B13:	JSR @SETUP	ITEST FOR ION RESET
01377	102001		ADC 0,0,SKP	TRY PI CYCLE
01400	001410		B13A	
01401	024777		LDA 1,.-1	
01402	044001		STA 1,1	
01403	061111		DOAS 0, .TTO	
01404	063511		SKPBZ .TTO	
01405	000777		JMP .-1	
01406	060177		NIOS CPU	ION STILL ON FOLLOWING
01407	101000		MOV 0,0	INTERRUPT
01410	063577	B13A:	SKPBZ CPU	ICHECK PTG1-PI RESET
01411	006070		EHALT	IGATE ON ION
01412	006072		JSR @LOOP	
01413	006071	B14:	JSR @SETUP	ITEST FOR ONE INSTRUCTION
01414	102001		ADC 0,0,SKP	ITO BE PERFORMED BEFORE
01415	001426		B14A	INTERRUPT OCCURS
01416	024777		LDA 1,.-1	
01417	044001		STA 1,1	
01420	061111		DOAS 0, .TTO	
01421	063511		SKPBZ .TTO	
01422	000777		JMP .-1	
01423	060177		NIOS CPU	
01424	102620		SUBZR 0,0	NO INTERRUPT. CHECK ION,
01425	006070		EHALT	INTRO, PI
01426	101125	B14A:	MOVZL 0,0,SNR	
01427	101003		MOV 0,0,SNR	IF NOT CORRECT FOLLOWING
01430	006070		EHALT	INTERRUPT. CHECK INTRO
01431	006072		JSR @LOOP	SYNC LOGIC FOR 1 INSTRUCTION
01432	006071	B15:	JSR @SETUP	IF NOT CORRECT FOLLOWING
01433	102620		SUBZR 0,0	INTERRUPT. CHECK INTRO
01434	040000		STA 0,0	SYNC LOGIC FOR 1 INSTRUCTION
01435	040001		STA 0,1	WAIT AFTER INTEN AND
01436	061111		DOAS 0, .TTO	BEFORE INTERRUPT
01437	063511		SKPBZ .TTO	
01440	000777		JMP .-1	
01441	060177		NIOS CPU	
01442	101001		MOV 0,0,SKP	IF ERROR
01443	001444		.+1	ICHECK PC TO MEM
01444	020777		LDA 0,.-1	DURING PI CYCLE
01445	024000		LDA 1,0	
01446	106414		SUB# 0,1,SR	
01447	006070		EHALT	
01450	006072		JSR @LOOP	

A 0023 .MAIN

01451	006071	B16:	JSR @SETUP	DEFER FAILED TO SET
01452	020067		LDA 0,IND4	
01453	040001		STA 0,1	
01454	102001		ADC 0,0,SKP	AT THE END OF PI-2
01455	001465		B16A	ICYCLE, CHECK 3 INPUT
01456	024777		LDA 1,-1	OR GATE TO PRODUCE
01457	044004		STA 1,4	DEFER SET.
01460	061111		DNAS 0,.TTO	
01461	063511		SKPBZ .TTO	
01462	000777		JMP .-1	
01463	060177		NIOS CPU	
01464	101010		MOV# 0,0	
01465	100014	B16A:	COM# 0,0,SZR	
01466	006070		EHALT	
01467	060277		NIOC CPU	
01470	006072		JSR @LOOP	
01471	006071	B17:	JSR @SETUP	IA INTERRUPT DEFERED
01472	102001		ADC 0,0,SKP	THROUGH LOCATION 0
01473	001505		B17A	NOT LOCATION 1. CHECK
01474	024777		LDA 1,-1	11 TO MA AT PI-2 TIME.
01475	044004		STA 1,4	CHECK ADD ONE LOGIC
01476	061111		DNAS 0,.TTO	1(A91) AND GATE
01477	063511		SKPBZ .TTO	1(PI2,TS3).
01500	000777		JMP .-1	
01501	060177		NIOS CPU	
01502	101010		MOV# 0,0	
01503	063477		SKPBN CPU	
01504	006070		EHALT	
01505	060277	B17A:	NIOC CPU	
01506	006072		JSR @LOOP	
01507	006071	B18:	JSR @SETUP	IA INTERRUPT OCCURED
01510	102001		ADC 0,0,SKP	TRUT FAILED TO STORE
01511	001522		B18A	ANYTHING IN LOC 0.
01512	024777		LDA 1,-1	CHECK INPUTS TO
01513	044004		STA 1,4	MEM MOD LOGIC.
01514	040000		STA 0,0	
01515	061111		DNAS 0,.TTO	
01516	063511		SKPBZ .TTO	
01517	000777		JMP .-1	
01520	060177		NIOS CPU	
01521	000404		JMP .+4	
01522	024000	B18A:	LDA 1,0	
01523	106415		SUB# 0,1,SNR	
01524	006070		EHALT	
01525	006072		JSR @LOOP	



A 0224 .MAIN

01526 006071 V001	JSR @SETUP	DI01A DID NOT
01527 102000	ADC 0,0	ICCHANGE THE CONTENTS
01530 060410	DIA 0,.TTI	IDF AC0. CHECK READ IN
01531 100015	COM# 0,0,SNR	ITHRU MEM TO AR-ACC
01532 006070	EHALT	
01533 006072	JSR @LOOP	
01534 074477	READS 3	ITF KSR TTY DON'T
01535 175100	MOVL 3,3	
01536 175102	MOVL 3,3,SZC	IDO THIS SECTION
01537 000561	JMP LE025	
01540 006071 V021	JSR @SETUP	ISTARTING THE PAPER
01541 060110	NIOS .TTI	ITAPE READER DID NOT
01542 006073	JSR @TIME	ICAUSE DONE TO SET.
01543 063610	SKPON .TTI	ITS THE READER IN THE
01544 101002	MOV 0,0,SZC	ISTART POSITION? TAPE IN
01545 006070	EHALT	IREADER? SWITCH IN THE
01546 006072	JSR @LOOP	ILINE POSITION?
01547 006071 V041	JSR @SETUP	ITHE SETTING OF
01550 060110	NIOS .TTI	ITTI DONE DID NOT
01551 006073	JSR @TIME	ICLEAR TT RDR BUSY.
01552 063610	SKPON .TTI	ICHECK TTI DONE(1)
01553 063510	SKPBZ .TTI	IINPUT TO CLOCK. ALSO
01554 006070	EHALT	IDATA INPUT SHOULD BE
01555 006072	JSR @LOOP	IGROUND.
01556 006071 V061	JSR @SETUP	IT/O RESET FAILED
01557 060110	NIOS .TTI	ITO CLEAR THE TTI
01560 006073	JSR @TIME	IDONE FLOP. CHECK
01561 063510	SKPBZ .TTI	IF40 5-6. ALSO INVERTER
01562 062677	IORST	IF39 10-11. TO THE
01563 063710	SKPDZ .TTI	IPRESET SIDE OF THE
01564 006070	EHALT	IDONE FLOP.
01565 006072	JSR @LOOP	
01566 006071 V081	JSR @SETUP	IA CLEAR PULSE TO
01567 060110	NIOS .TTI	ITHE TTI LOGIC FAILED
01570 006073	JSR @TIME	ITO CLEAR DONE.
01571 063510	SKPBZ .TTI	ICHECK E40 4-6.
01572 060210	NIOC .TTI	ITHE OF GATE TO
01573 063710	SKPDZ .TTI	IPRESET SIDE OF DONE.
01574 006070	EHALT	
01575 006072	JSR @LOOP	

A 0025 .MAIN

01576	006071	V101	JSR @SETUP	IA START PULSE TO
01577	060110		NIOS .TTI	ITHE TTI LOGIC FAILED
01600	006073		JSR @TIME	ITO CLEAR DONE.
01601	063510		SKPBZ .TTI	ICHECK E40 3-6, THE
01602	060110		NIOS .TTI	IOR GATE TO RESET SIDE
01603	063710		SKPDZ .TTI	IOF DONE.
01604	006070		EHALT	
01605	006073		JSR @TIME	
01606	063510		SKPBZ .TTI	
01607	006072		JSR @LOOP	
01610	006071	V121	JSR @SETUP	IAFTER A I/O
01611	061477		INTA 0	IRESET INTA SHOULD
01612	101004		MOV 0,0,SZR	IREAD NO BITS.
01613	006070		EHALT	
01614	006072		JSR @LOOP	
01615	006071	V141	JSR @SETUP	IAWITH THE DONE FLAG
01616	102400		SUB 0,0	IRESET INTA SHOULD
01617	062077		MSKO 0	IREAD BACK A DEVICE
01620	062677		IORST	ICODE, CHECK TTI
01621	060110		NIOS .TTI	INT REQ FLOP.
01622	006073		JSR @TIME	
01623	063510		SKPRZ .TTI	
01624	061477		INTA 0	
01625	101005		MOV 0,0,SNR	
01626	006070		EHALT	
01627	006072		JSR @LOOP	
01630	006071	V161	JSR @SETUP	ICHECK THAT I/O RESET
01631	102520		SUBZL 0,0	WILL RESET TTI INT
01632	101100		MOVL 0,0	DISABLE.
01633	062077		MSKO 0	IRESET THE FLOP
01634	062677		IORST	ITRY TO CLEAR
01635	060110		NIOS .TTI	IRESET DONE.
01636	006073		JSR @TIME	
01637	063510		SKPRZ .TTI	
01640	061477		INTA 0	IREXPECT DEVICE CODE.
01641	101005		MOV 0,0,SNR	
01642	006070		EHALT	
01643	006072		JSR @LOOP	
01644	006071	V181	JSR @SETUP	IA MSKO INSTRUCTION
01645	102400		SUB 0,0	WITHOUT A BIT 14
01646	062077		MSKO 0	ISHOULD NOT SET
01647	060110		NIOS .TTI	ITTI INT DISABLE, NO
01650	006073		JSR @TIME	IDEVICE CODE WAS READ,
01651	063510		SKPBZ .TTI	IRACK ON INTA HOWEVER.
01652	061477		INTA 0	ISUGGEST THE DATA INPUT
01653	101005		MOV 0,0,SNR	ITTO TTI INT DISABLE
01654	006070		EHALT	IFAILED.
01655	006072		JSR @LOOP	

01656	006071	V20:	JSR @SETUP	;TTT INT DISARLF FAILED
01657	102520		SUBZL 0,0	;TO SET ON A MSKO
01660	101120		MOVZL 0,0	;INSTRUCTION OR , THE
01661	062077		MSKO 0	;AND GATE (TTI INT DISARLF
01662	060110		NIOS .TTI	; (0),TTI DONE(1)) FAILED.
01663	006073		JSR @TIME	;MSKO SHOULD PREVENT
01664	063510		SKPBZ .TTI	;INTA FROM READING
01665	061477		INTA 0	;DEVICE CODE.
01666	101004		MOV 0,0,SZR	
01667	006070		EHALT	
01670	006072		JSR @LOOP	
01671	006071	V22:	JSR @SETUP	;CHECK THAT I/O RESET
01672	102620		SUBZR 0,0	;WILL CLEAR TTI
01673	040001		STA 0,1	;INT REQ.
01674	060110		NIOS .TTI	
01675	006073		JSR @TIME	;WAIT FOR DONE
01676	063510		SKPBZ .TTI	
01677	060177		NIOS CPU	;ENABLE INTERRUPT
01700	062477		DIC 0,CPU	;I/O RESET
01701	063477		SKPBN CPU	;CHECK THAT NO
01702	006070		EHALT	;INTERRUPT OCCURED.
01703	060277		NIOS CPU	
01704	006072		JSR @LOOP	
01705	006071	V24:	JSR @SETUP	;CHECK THE READ A
01706	060110		NIOS .TTI	;CHARACTOR TIME TO
01707	006073		JSR @TIME	;BE WITHIN 5% OF
01710	063510		SKPBZ .TTI	;100MS.
01711	024076		LDA 1,CTIM	
01712	106423		SUBZ 0,1,SNC	
01713	124400		NEG 1,1	
01714	030141		LDA 2,PC5	
01715	132433		SUBZ# 1,2,SNC	
01716	006070		EHALT	
01717	006072		JSR @LOOP	
	001720		LEO25=.	
01720	006150	V26:	JSR @ICRLF	;END OF PASS MESSAGE
01721	006150		JSR @ICRLF	
01722	006151		JSR @IMESS	;MESSAGE PASS
01723	002115		PMESS	
01724	006150		JSR @ICRLF	
01725	063611		SKPDN .TTO	
01726	000777		JMP .-1	
01727	034045		LDA 3,45	;MOTHER HEN STUFF
01730	011403		ISZ 3,3	
01731	000401		JMP .+1	
01732	002401		JMP 0,+1	
01733	000550		T20	

A 0027 .MAIN

01734	021400	TIMPR:	LDA 0,0,3	;TIME THE INST
01735	040405		STA 0,0,+5	;FOLLOWING THE CALL.
01736	102040		ADCO 0,0	
01737	102410		SUB# 0,0	
01740	101402		INC 0,0,SZC	
01741	001401		JMP 1,3	
01742	000000		0	
01743	000774		JMP 0,-4	
01744	040100		STA 0,TIMEX	
01745	001401		JMP 1,3	
01746	054413	ENTER:	STA 3,LOOPR	;ITERATION RETURN
01747	034406		LDA 3,ITR	;THIS TEST INITIALIZES
01750	054406		STA 3,ITRCT	;EACH ROUTINE.
01751	176400		SUB 3,3	
01752	054405		STA 3,ESWIT	
01753	062677		IORST	;I/O RESET
01754	002405		JMP 0,LOOPR	
01755	000003	ITR:	3	
01756	000000	ITRCT:	0	
01757	000000	ESWIT:	0	
01760	000000	RETURN:	0	
01761	000000	LOOPR:	0	
01762	054776	ERR:	STA 3,RETURN	;ERROR
01763	034774		LDA 3,ESWIT	
01764	175004		MOV 3,3,SZR	
01765	002773		JMP 0,RETURN	;NOT FIRST ERROR
01766	034772		LDA 3,RETURN	;SET ERROR SWITCH
01767	054770		STA 3,ESWIT	;C(3)=PC+1 OF THE
01770	063077		HALT	;ERROR
01771	002767		JMP 0,RETURN	

A 0028 .MAIN

01772	054766	CYCLE:	STA 3,RETURN	;ITERATION ROUTINE
01773	014763		DSZ ITRCT	;END OF EACH TEST.
01774	000410		JMP CYCTS	;NOT 10 ITERATIONS
01775	034760		LDA 3,ITR	;RESET ITERATION
01776	054760		STA 3,ITRCT	;COUNTER. IF ERROR
01777	034760		LDA 3,ESWIT	;OCCURED STAY IN LOOP.
02000	062677		IORST	
02001	175004		MOV 3,3,SZR	
02002	002757		JMP @LOOPR	;ITERATE PROGRAM
02003	002755		JMP @RETURN	;GO TO NEXT PROG.
02004	034753	CYCTS:	LDA 3,ESWIT	;IF A ERROR
02005	175004		MOV 3,3,SZR	;LOOK AT SWITCH 0
02006	074477		READS 3	
02007	062677		IORST	;IF SWITCH SET PROCEED.
02010	175113		MOVL# 3,3,SNC	
02011	002750		JMP @LOOPR	;ITERATE
02012	002746		JMP @RETURN	;NEXT TEST.
02013	054134	MESS:	STA 3,MESSR	;PRINT A TEXT MESSAGE
02014	010134		ISZ MESSR	
02015	031400		LDA 2,0,3	;C(2) POINTS TO MESSAGE
02016	024135		LDA 1,C377	;8 BIT MASK
02017	021000		LDA 0,0,2	;C(2)=DATA WORD
02020	125112		MOVL# 1,1,S7C	
02021	123701		ANDS 1,0,SKP	
02022	123401		AND 1,0,SKP	;C(0)=DATA CHARACTER
02023	151400		INC 2,2	;INC TO NEXT WORD
02024	124000		COM 1,1	;FLIP MASK
02025	004403		JSR CHAR	;PRINT
02026	000771		JMP MESS+4	;ANOTHER
02027	002134		JMP @MESSR	;LAST
02030	101005	CHAR:	MOV 0,0,SNR	;PRINT A CHARACTER FROM
02031	001401		JMP 1,3	;C(0)R. EXIT +2 IF NULL
02032	063511		SKPBZ .TTO	
02033	000777		JMP .-1	;WAIT IF TTO BUSY
02034	061111		DOAS 0,.TTO	;XMITT CHARACTER
02035	001400		JMP 0,3 ;EXIT +1	
02036	054134	CRLF:	STA 3,MESSR	;PRINT A CARRIAGE
02037	020136		LDA 0,C215	;RETURN AND A LINE
02040	004770		JSR CHAR	;FEED SEQUENCE.
02041	020137		LDA 0,C12	
02042	004766		JSR CHAR	
02043	002134		JMP @MESSR	
02044	006150	ALPH:	JSR @ICRLF	;ALPHA TEST MESSAGE
02045	006151		JSR @IMESS	
02046	002050		ALP	;MESSAGE STORAGE AREA
02047	000775		JMP ALPH	

A 0020 .MAIN

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ALP: .TXTE I
02050 046515 MMM."#3%&'()*
02051 027115
02052 121442
02053 122444
02054 023646
02055 124450
02056 025652 +,=,./01234
02057 026654
02060 127456
02061 130460
02062 031662
02063 032664 56789!;"
02064 133466
02065 034670
02066 135472
02067 037675 ?@ABCDEFGHIJ
02070 040700
02071 141502
02072 142504
02073 043706
02074 144510
02075 045712 KLMNOPQRST
02076 046714
02077 147516
02100 150520
02101 051722
02102 052724 UVWXYZ] [ ^
02103 153526
02104 054730
02105 156532
02106 116333
02107 057736 00?0?0?0?0!
02110 037700
02111 037700
02112 037700
02113 037700
02114 000300

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PMESS: .TXTE IPASSI
02115 040520
02116 051523
02117 000000

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02120 000000 TABLE: 0
.END

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